



## **Ms. Supriya S. Saste**

M.E. (Digital System), B.E. (Electronics and Telecommunication Engineering)

Assistant Professor

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**Experience: 5 years**

### **Research Interest**

VLSI Design, Communication System, Digital Electronics.

### **Research Publications and Citations**

International Journal : 02 International Conferences : 01 National Conferences : 01

### **Workshop/Seminar Attended**

- One Week FDP on Data Sciences.
- One Week FDP on CYBER SECURITY.
- Two week AICTE-FDP on “Recent Trends in Signal Representation: A Multirate Approach” during 6<sup>th</sup> Nov- 17<sup>th</sup> Nov 2017.
- Attended International Conference on “VLSI Design Trends”
- One week workshop on “An Embedded generations interface with real world: A practical Approach”
- Attended MATLAB workshop
- Attended PCB Designing workshop by CAMPUS COMPONENT

### **Subjects Taught**

- Electrical and Electronics Engineering
- Industrial Management/Business Management
- VLSI Design & Technology
- Mobile Communication
- Digital Communication

### **Publications**

- Published a paper titled “Cloud Ready Ultrasonic Sensor (HC-SR04) and DHT11 Using Raspberry-pi and ThingSpeak” in Journal of Network Security Computer Networks (MAT Journals)
- Published a paper on “Cluster of Image Quality Measures for Image Enhancement” in Journal of Research in Image and Signal Processing.
- Published a paper titled “Smart jacket based on IoT review” in International Journal Of Advance Research, Ideas And Innovations In Technology.
- Presented a paper titled “Fast Computation Based on Radix-4 Algorithm” in e-PGpex-2016 at RSCOE, Pune.
- Presented a paper at International Level Conference titled “Design and Implementation of Radix-4 Based Arithmetic Operations” in ICCASP 2016, BATU, Lonere.
- Published a paper titled “Design and Implementation of Radix-4 Based Multiplication on FPGA” in International Journal of Engineering Research & Technology.