

## Faculty Profile

	<b>Name:</b> Prof. Pooja Pawar <b>Designation:</b> Assistant Professor <b>Email- id:</b> poojapawar.kjcoemr@kjei.edu.in	
<b>Qualification</b>	M.Tech (Electronics)	
<b>Department</b>	ELECTRONOICS ENGINEERING-VLSI DESIGN & TECHNOLOGY	
<b>Area of Interest</b>	Digital Electronics	
<b>Experience</b>	<b>Academic</b>	1 Years
	<b>Industry</b>	3
<b>Research</b>	<b>Patent</b>	
	<b>Copyright</b>	
	<b>National Conference</b>	
	<b>International Conference</b>	
	<b>National Journal</b>	
	<b>International Journal</b>	1
	<b>Book Chapter</b>	
	<b>Books</b>	
<b>Professional Affiliations</b>	JRF (ISRO)	
<b>Awards &amp; Recognitions</b>	KJ's Educational Institute, K J COLLEGE OF ENGINEERING AND MANAGEMENT RESEARCH	
<b>Responsibilities at KJCOEMR</b>	<b>Institute Level</b>	
	<b>Department Level</b>	ERP Coordinator, Exam Coordinator, Class-teacher
<b>Other Responsibilities</b>		
<b>Vidwan Profile Link</b>		
<b>ORCID ID</b>		
<b>Google Scholar Link</b>		